09,925,351

UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

DATED

: 6,750,605 B2

: June 15, 2004

INVENTOR(S) : Chad Byron Moore

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

Lines 5-10, after "REFERENCE TO RELATED APPLICATIONS" should read:

-- This is a divisional patent application of Ser. No. 09/299,371, filed Apr. 26, 1999, entitled "FRIT-SEALING PROCESS USED IN MAKING DISPLAYS", which is now U.S. Pat. No. 6,354,899 issued Mar. 12, 2002, The aforementioned patent is hereby incorporated herein by reference. --

Signed and Sealed this

Thirtieth Day of November, 2004

JON W. DUDAS Director of the United States Patent and Trademark Office